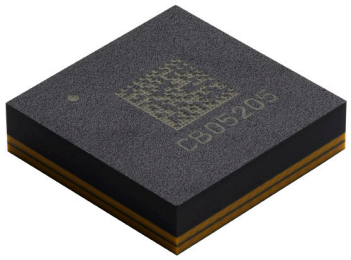
PRODUCT BRIEF

MM5620 & MM5622

64 Gbps High-Speed Differential Switch



The Menlo Micro's Differential Dual DP3T switches support the high-speed differential signal switching required in PCIe Gen 6, SerDes and other applications. The MM5620/MM5622 switches are based on Menlo Micro's Ideal Switch® technology and can operate at 64 Gbps with a bandwidth of 20 GHz for high-performance applications.

The MM5620/MM5622 has low insertion loss, fast switching speed, and can operate with greater than 3 billion switching cycles. The MM5620's/ MM5622's integrated charge pump and driver can be controlled through SPI or GPIO interfaces by a host processor.

The design fully integrates the loopback capacitors for the MM5620 switch and offers substantial reductions in size when compared with other high speed switch solutions.

ideal switch®

FEATURES

- DC to 20 GHz range
- DP3T (differential mode) with LoopBack Mode
- Normally Open, Reflective actuator
- Low Insertion Loss: -1.5dB @ 16 GHz
- Integrated charge pump and driver eliminates the requirement for external biasing and driver circuitry
- Fully controllable ports for low, medium, and high data rate signal routing
- High Reliability: Greater than 3 billion switching operations
- 8.2 x 8.2 mm LGA Package

APPLICATIONS

- High-Speed Data Digital SoC Testing
- ATE-Device Interface Boards
- O/E Module Testing
- Differential Switch Matrices

MARKETS

- Automated Test Equipment
- Measurement Equipment
- Semiconductor Final Package Test
- Compliance and Loopback Test

FIG. 1 MM5620 Functional Block Diagram

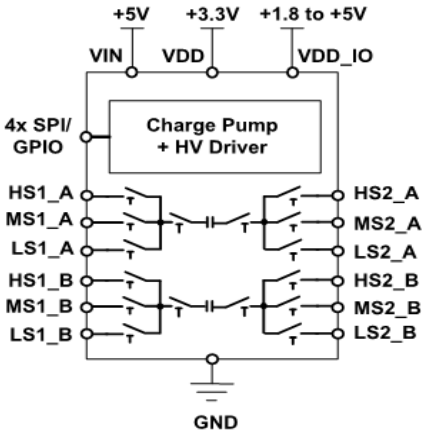


FIG. 2 MM5622 Functional Block Diagram

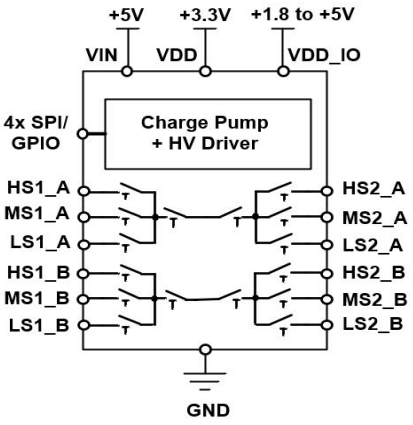


FIG. 3 MM5620/MM5622 Evaluation Board

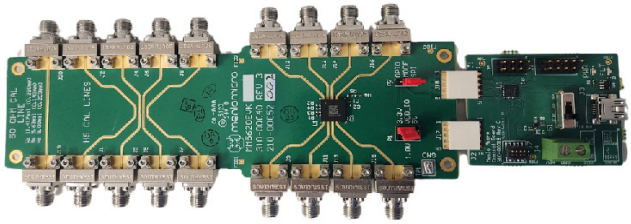
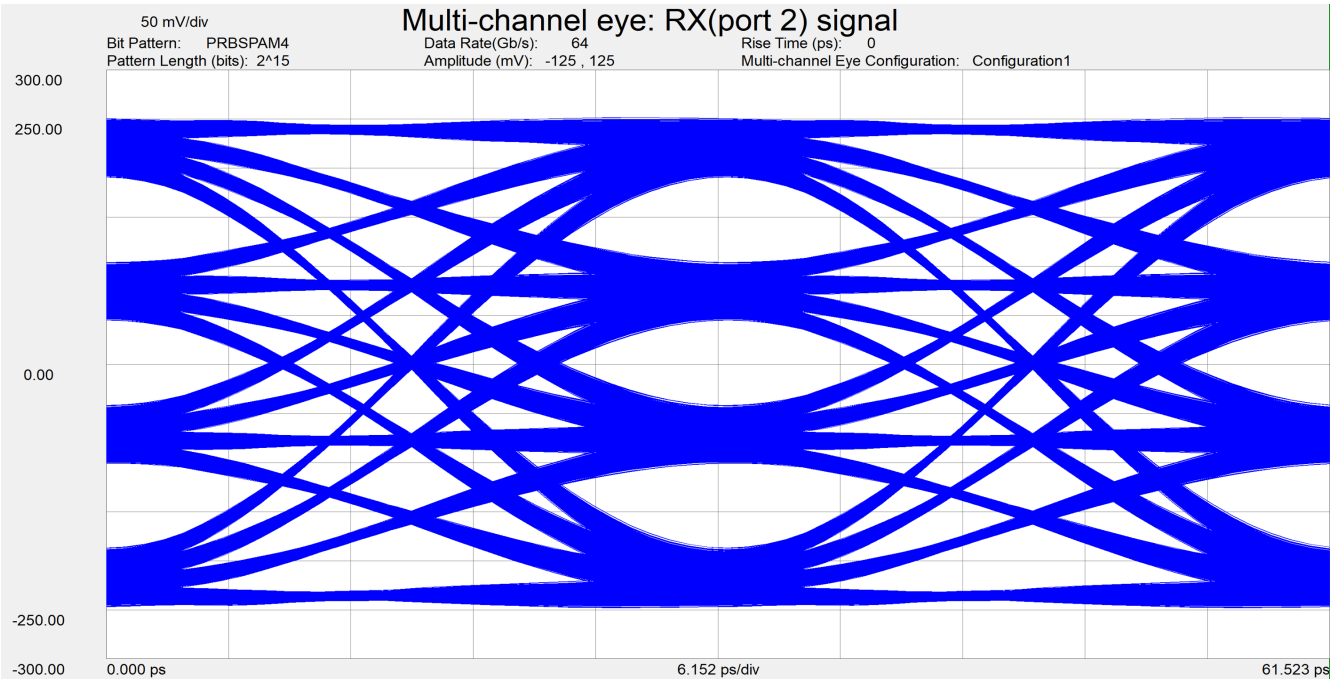


FIG. 4 PLTS2023: PCIe Gen6: +/-250mV, PAM4, 32GBaud (64Gbps), PRBS 2^{15}-1



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